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Fig.1  
PRIOR ART

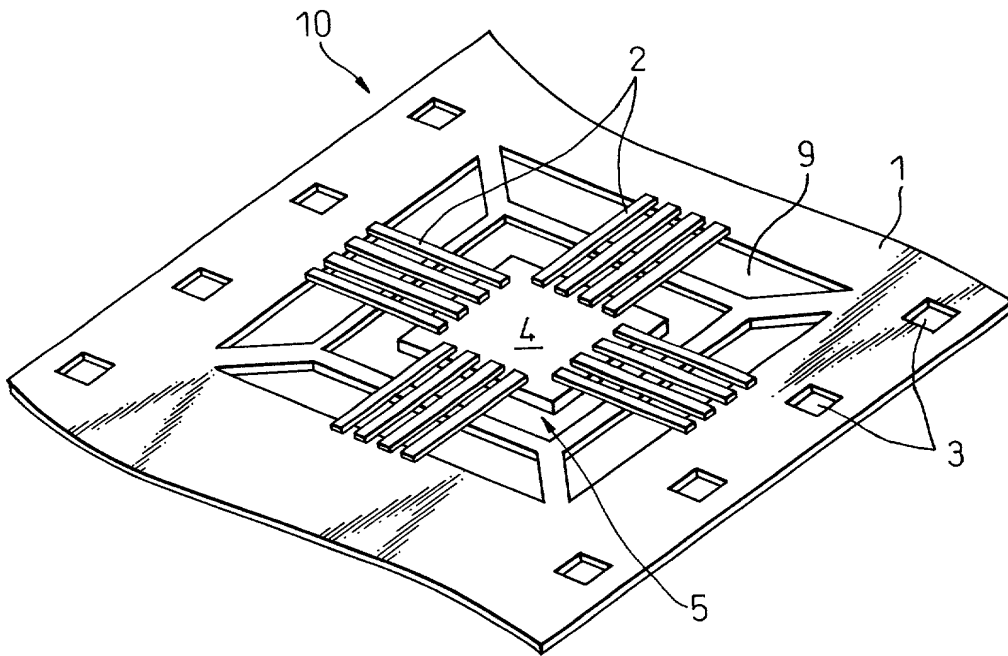
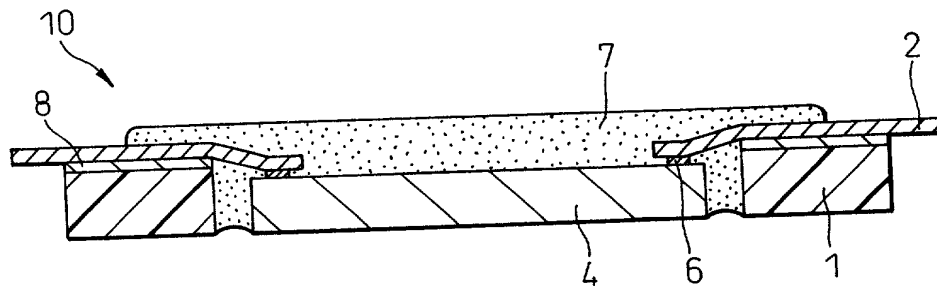
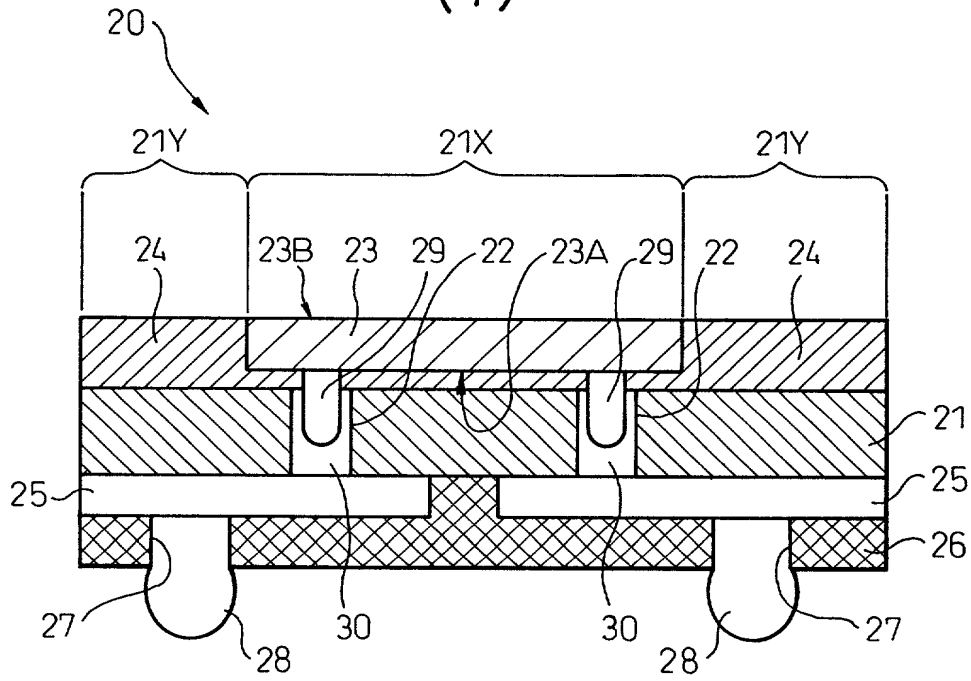


Fig.2  
PRIOR ART



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Fig.3  
(1)



(2)

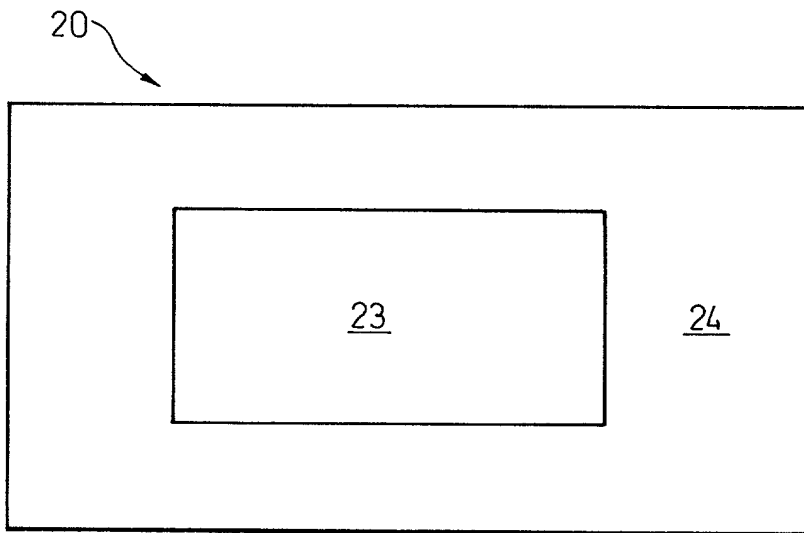


Fig. 4

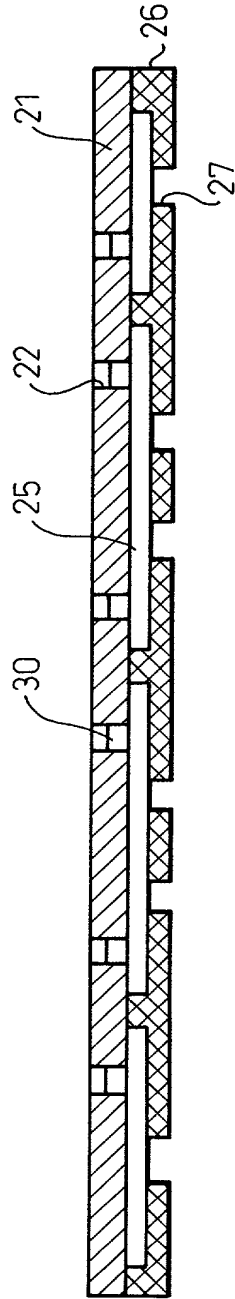


Fig. 5

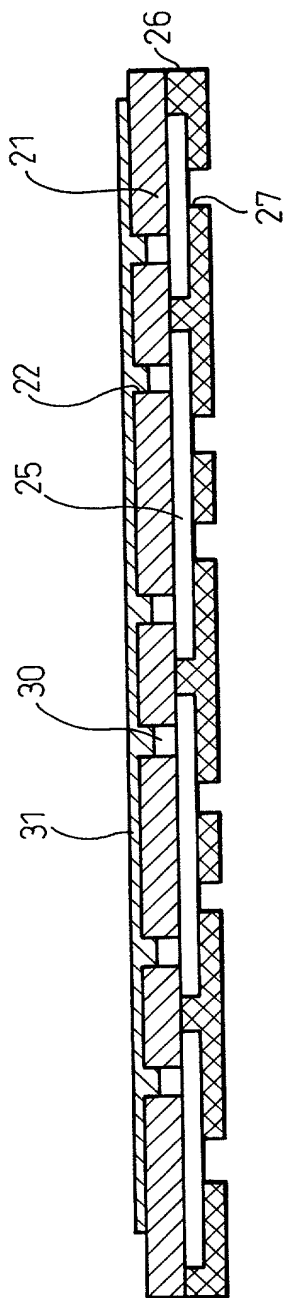






Fig. 8

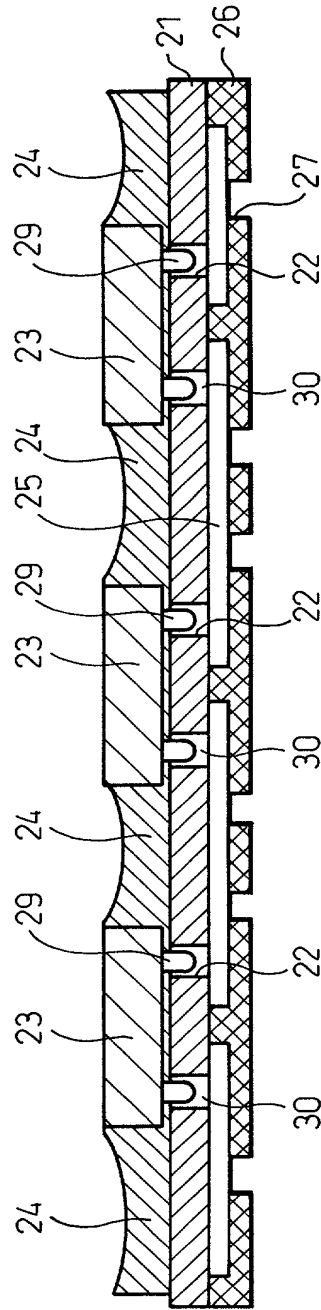


Fig.9

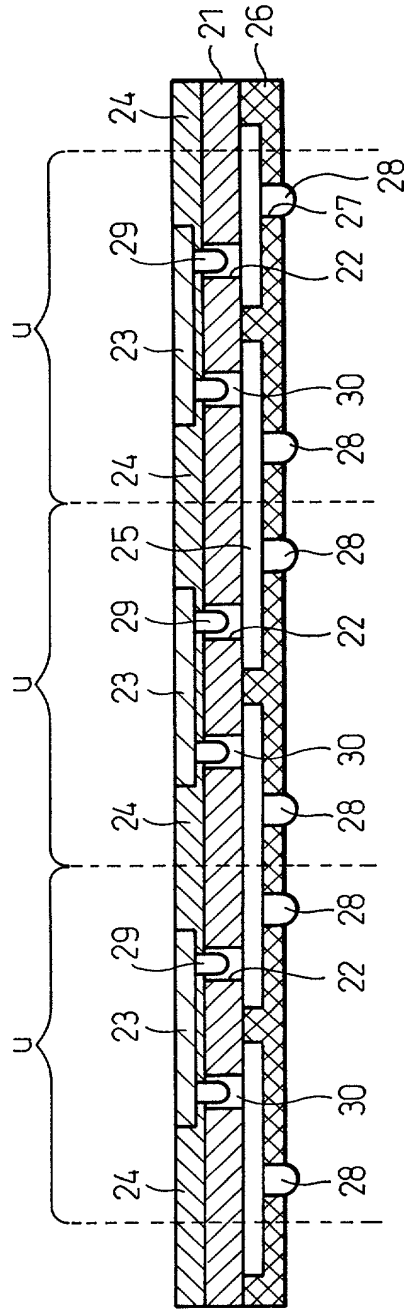
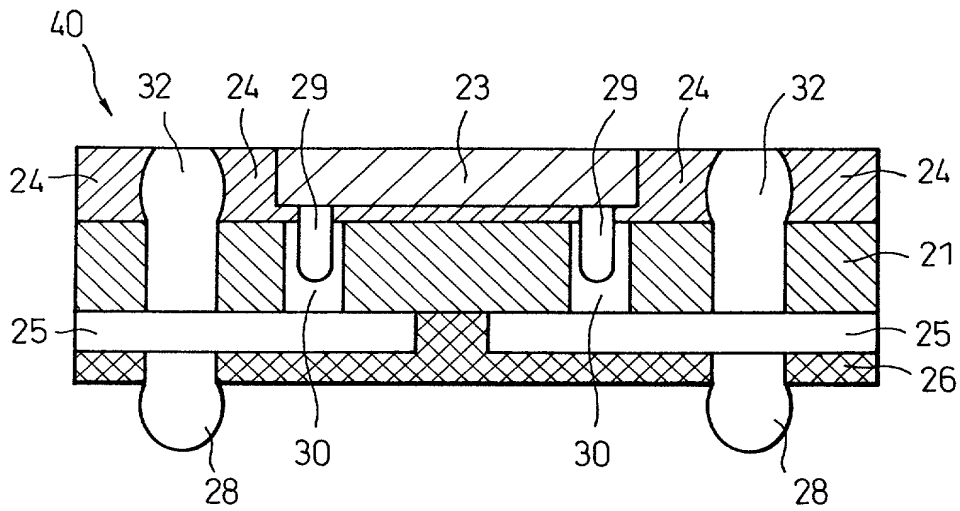




Fig.10

(1)



(2)

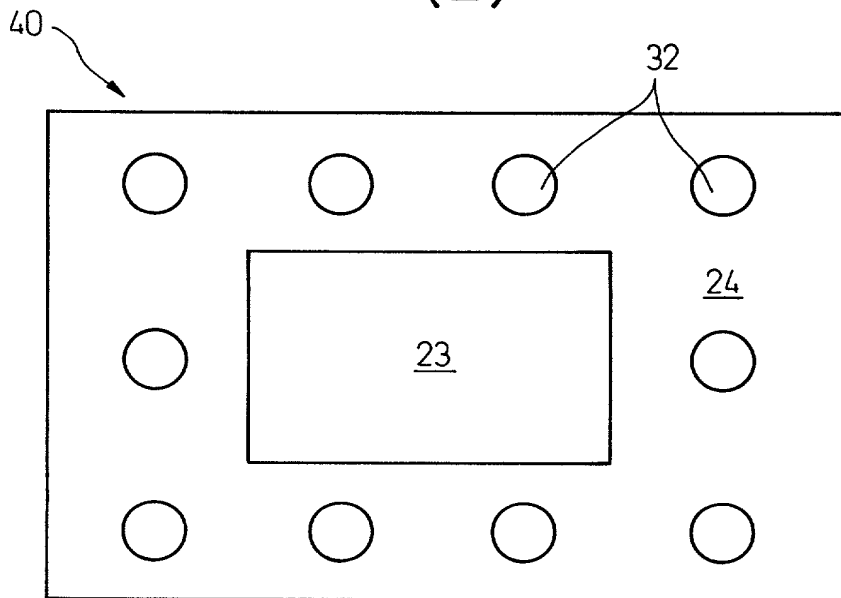


Fig.11

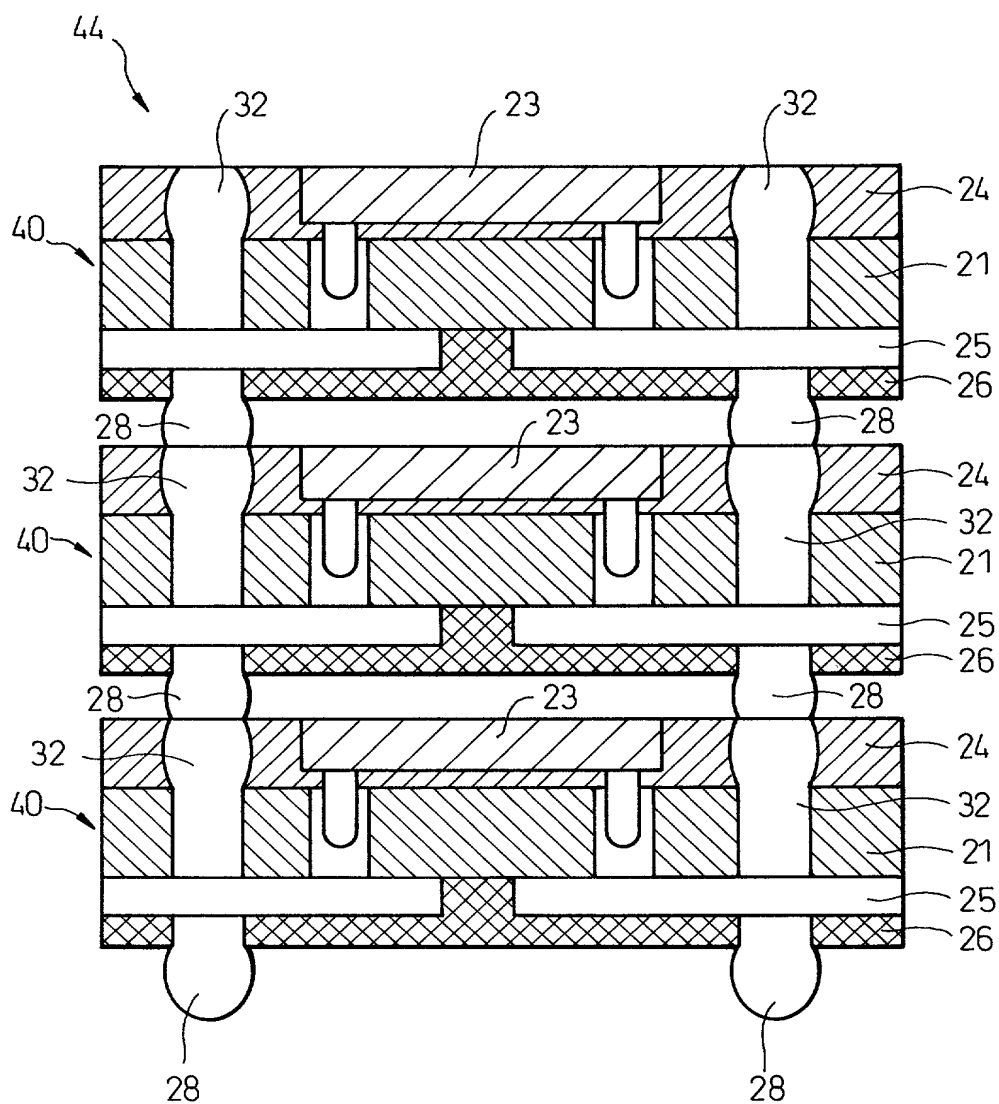


FIG. 11 is a cross-sectional view of the device of FIG. 10, taken along line 11-11 of FIG. 10, showing the device in a second state of operation. In this state, the device is configured to receive and process a second input signal. The device includes a first input terminal 10, a second input terminal 11, a first output terminal 12, and a second output terminal 13. The device is configured to receive a first input signal at the first input terminal 10 and a second input signal at the second input terminal 11. The device is configured to output a first output signal at the first output terminal 12 and a second output signal at the second output terminal 13. The device is configured to receive and process the second input signal in a manner similar to the manner in which the device receives and processes the first input signal.

Fig.12

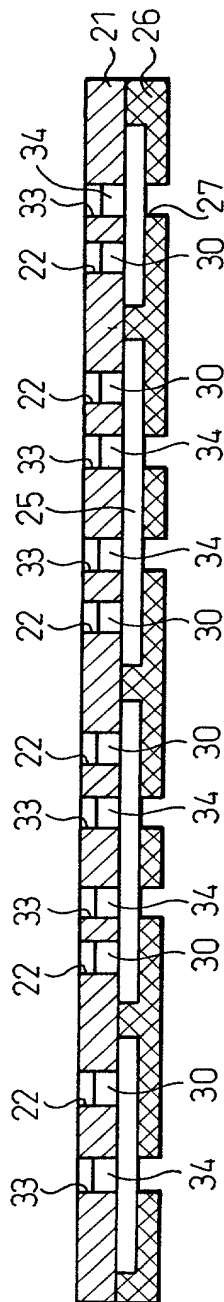


Fig.13

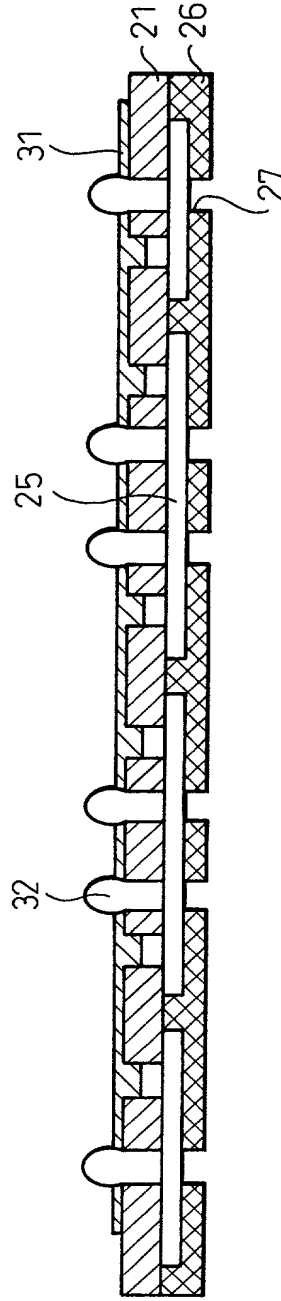
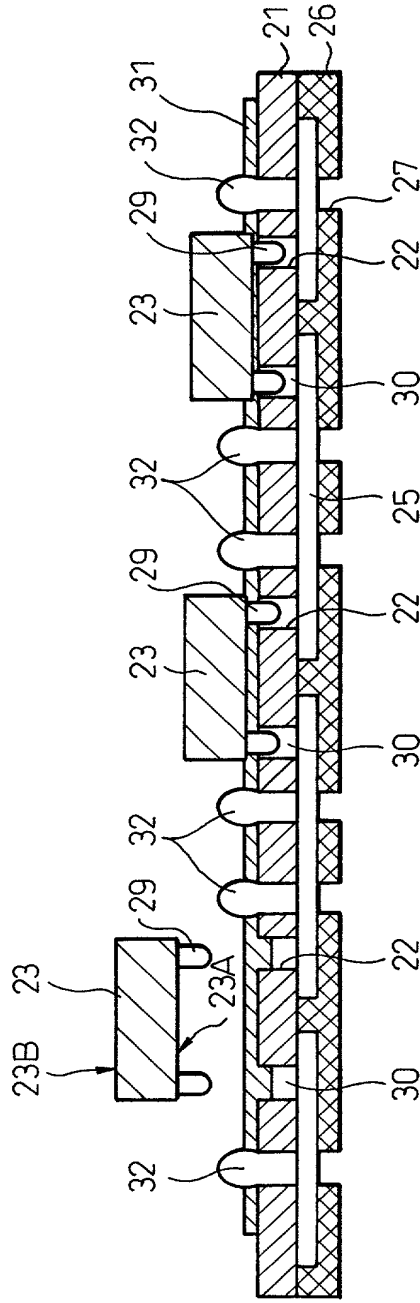


Fig.14



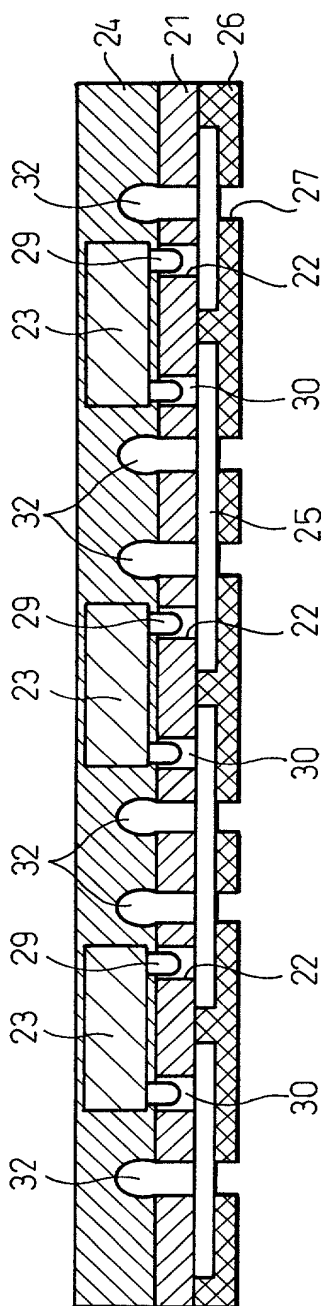


Fig. 15

Fig.16

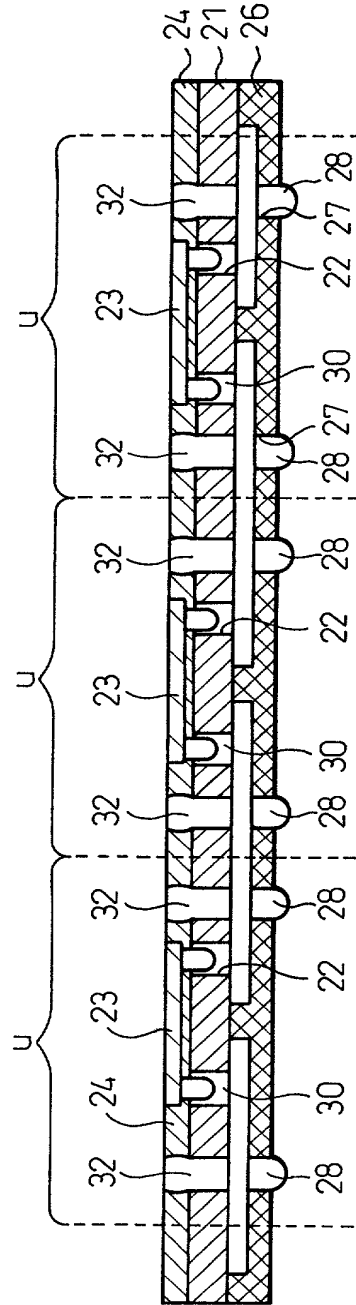
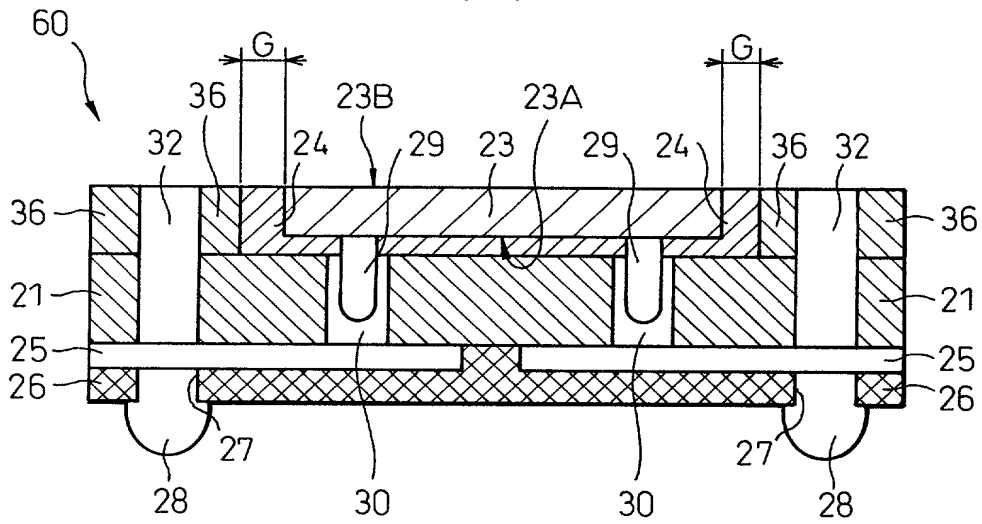


Fig.17

(1)



(2)

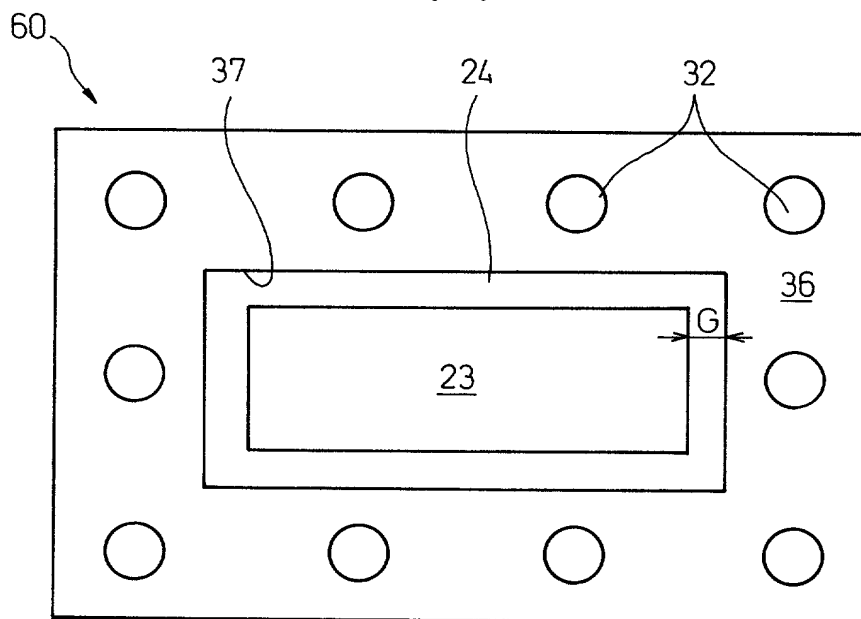




Fig.18

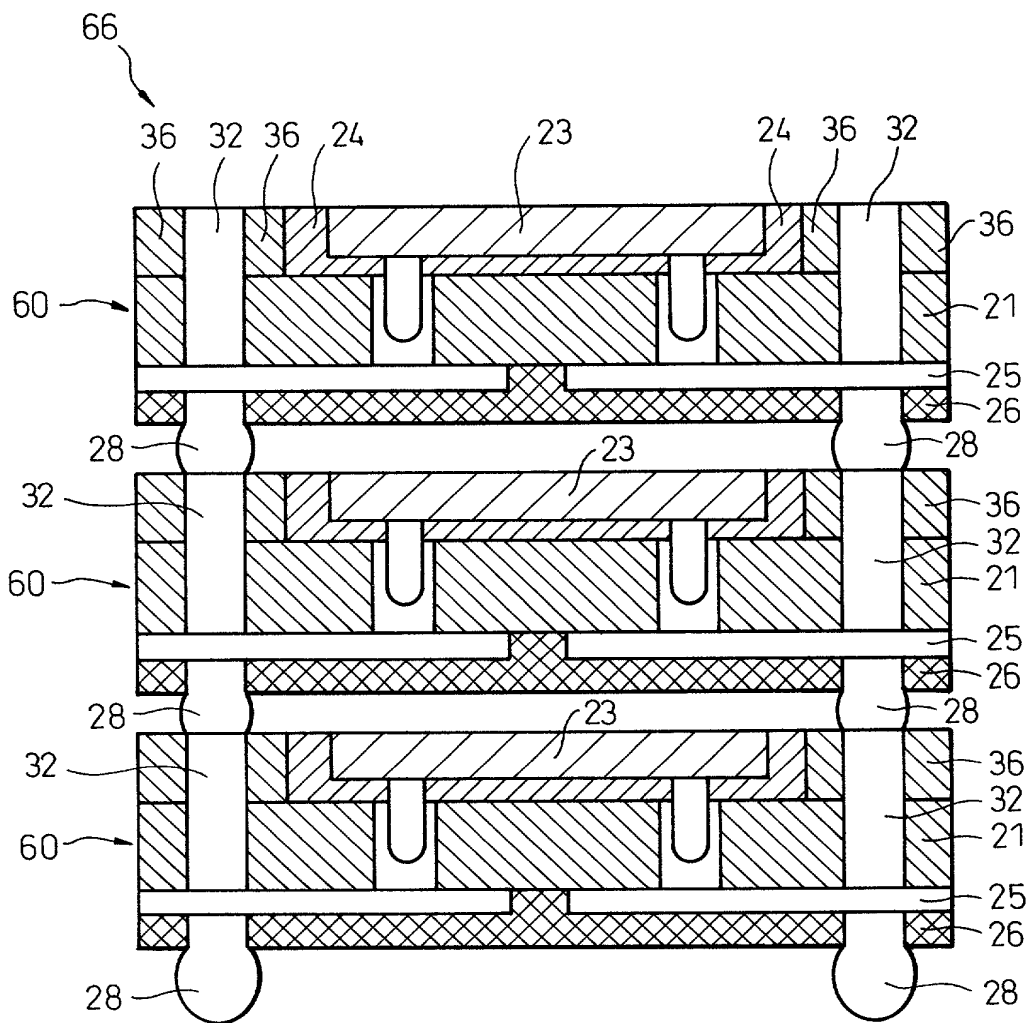


Fig.19

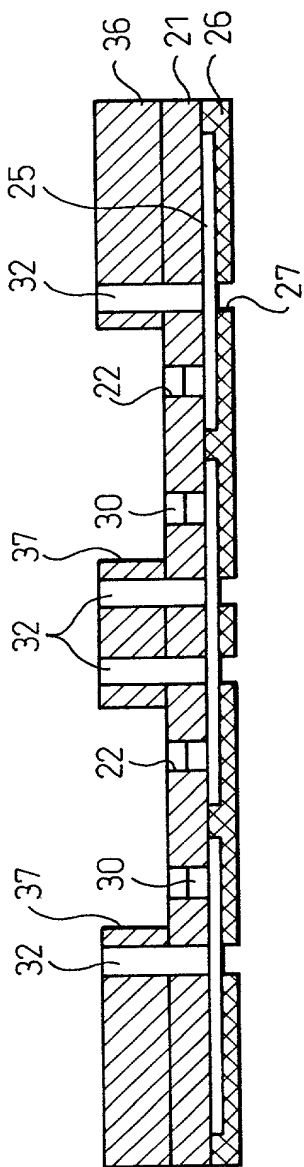


Fig.20

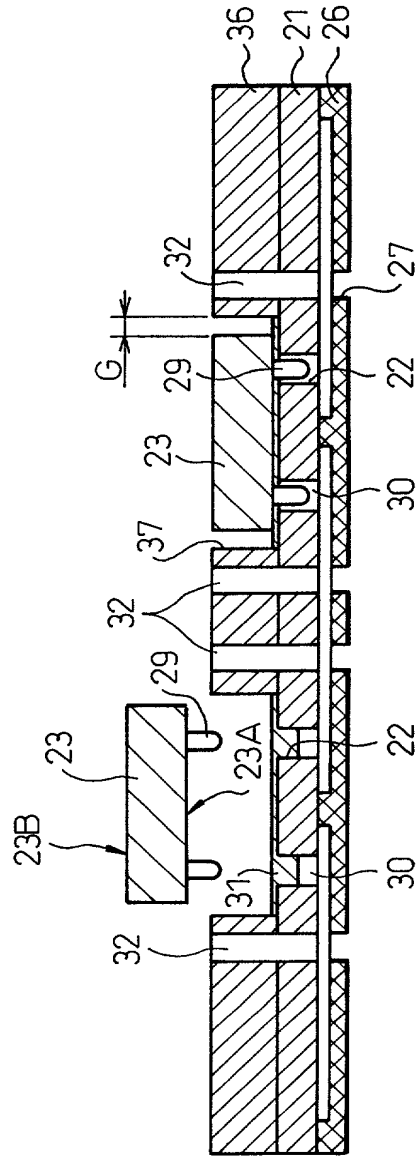


Fig. 21

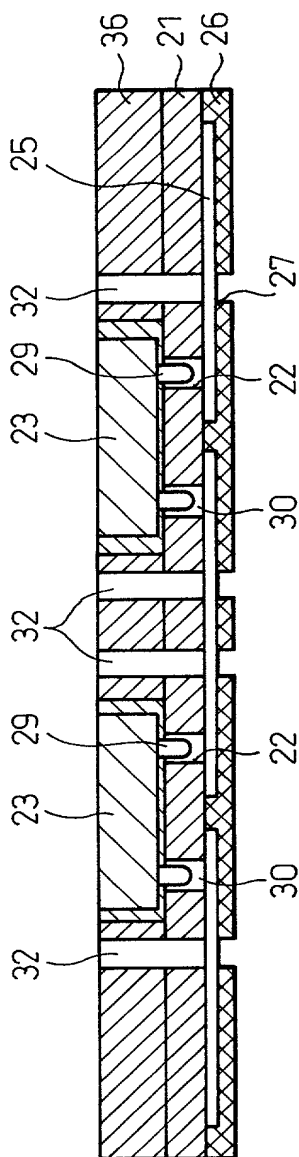


Fig. 22

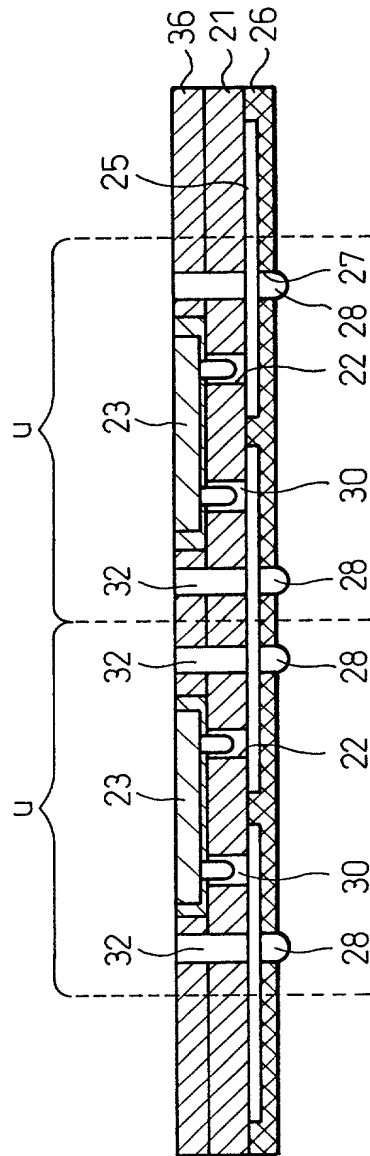
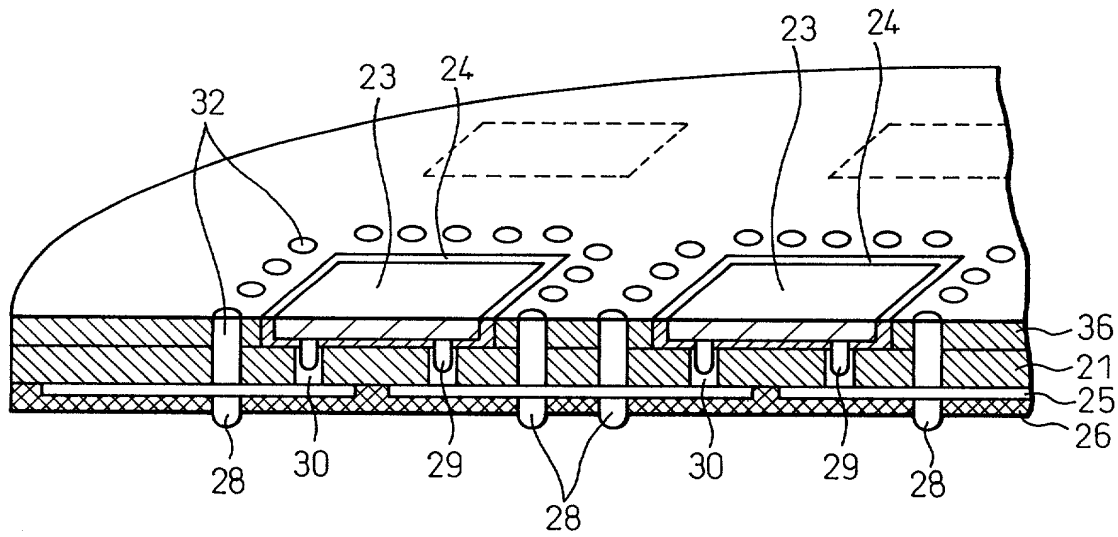
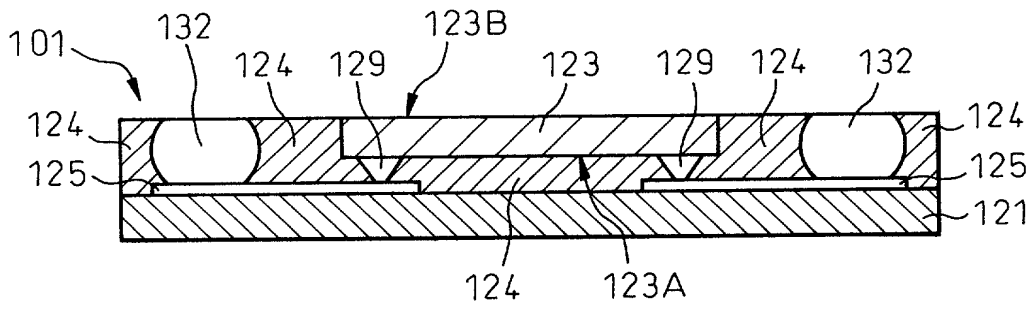


Fig.23

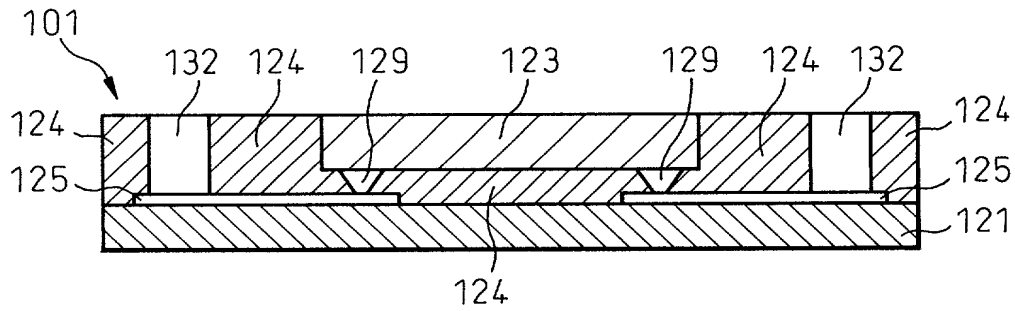


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Fig. 24  
(1)



(2)



(3)

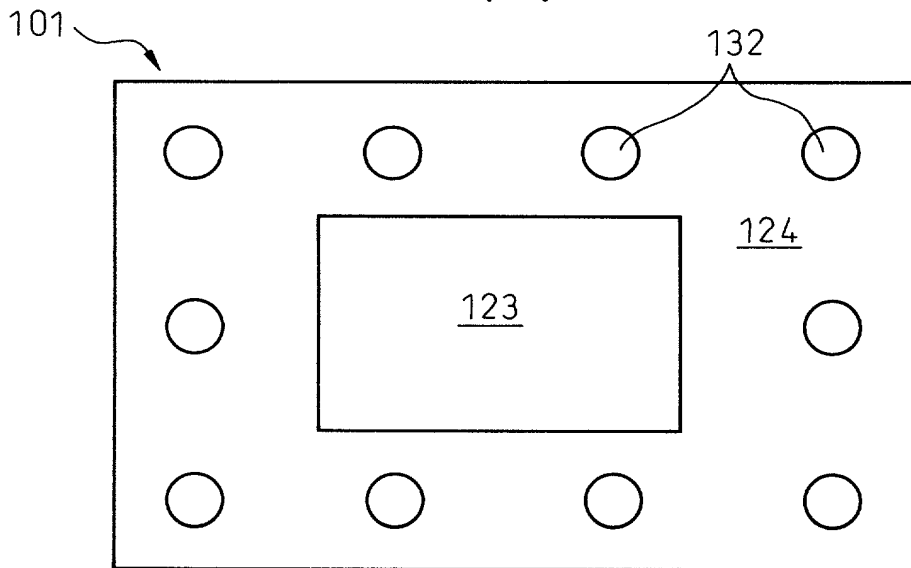


Fig. 25

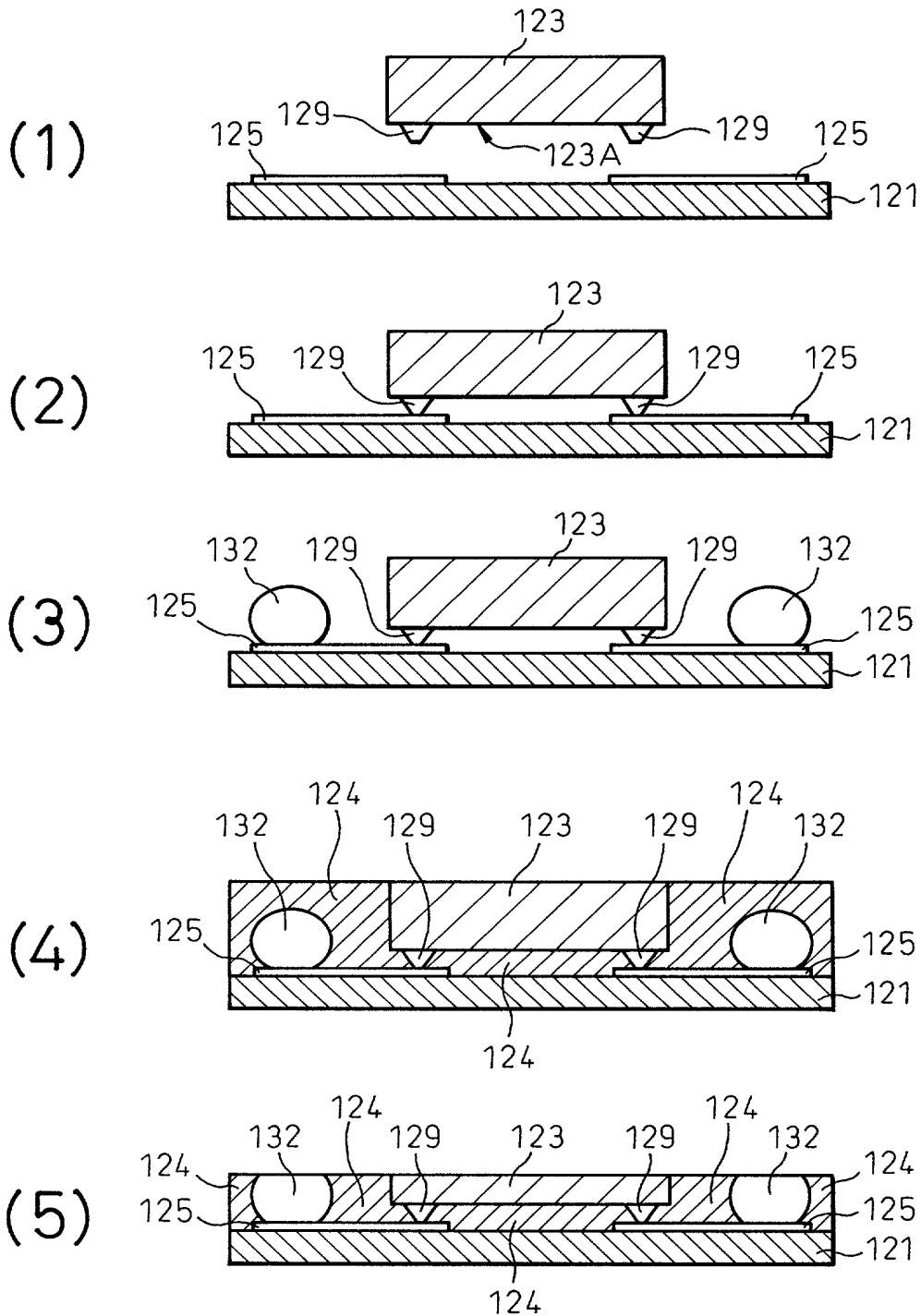
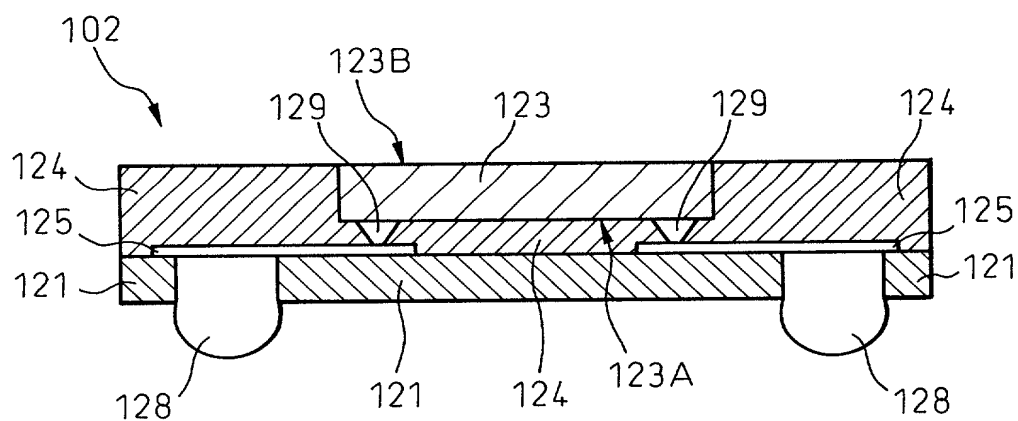




Fig.26

(1)



(2)

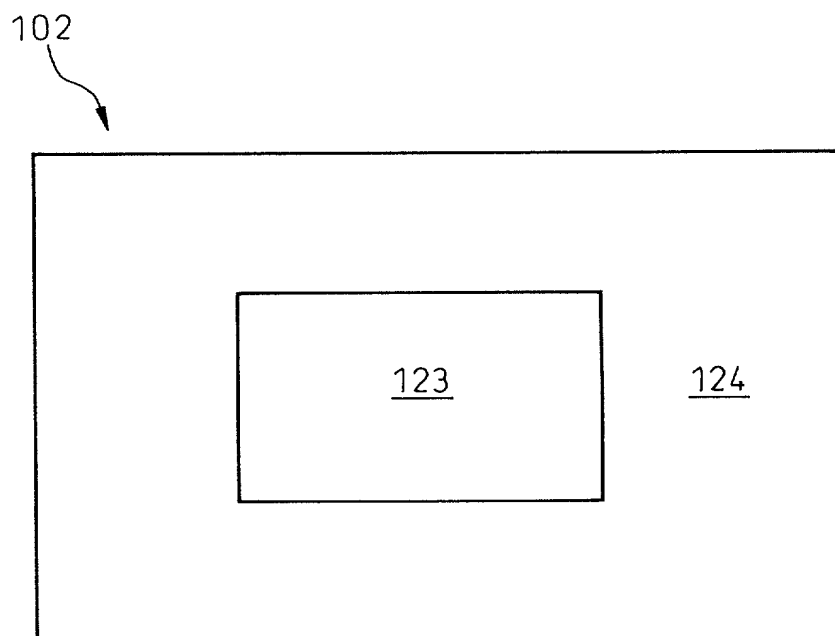


Fig.27

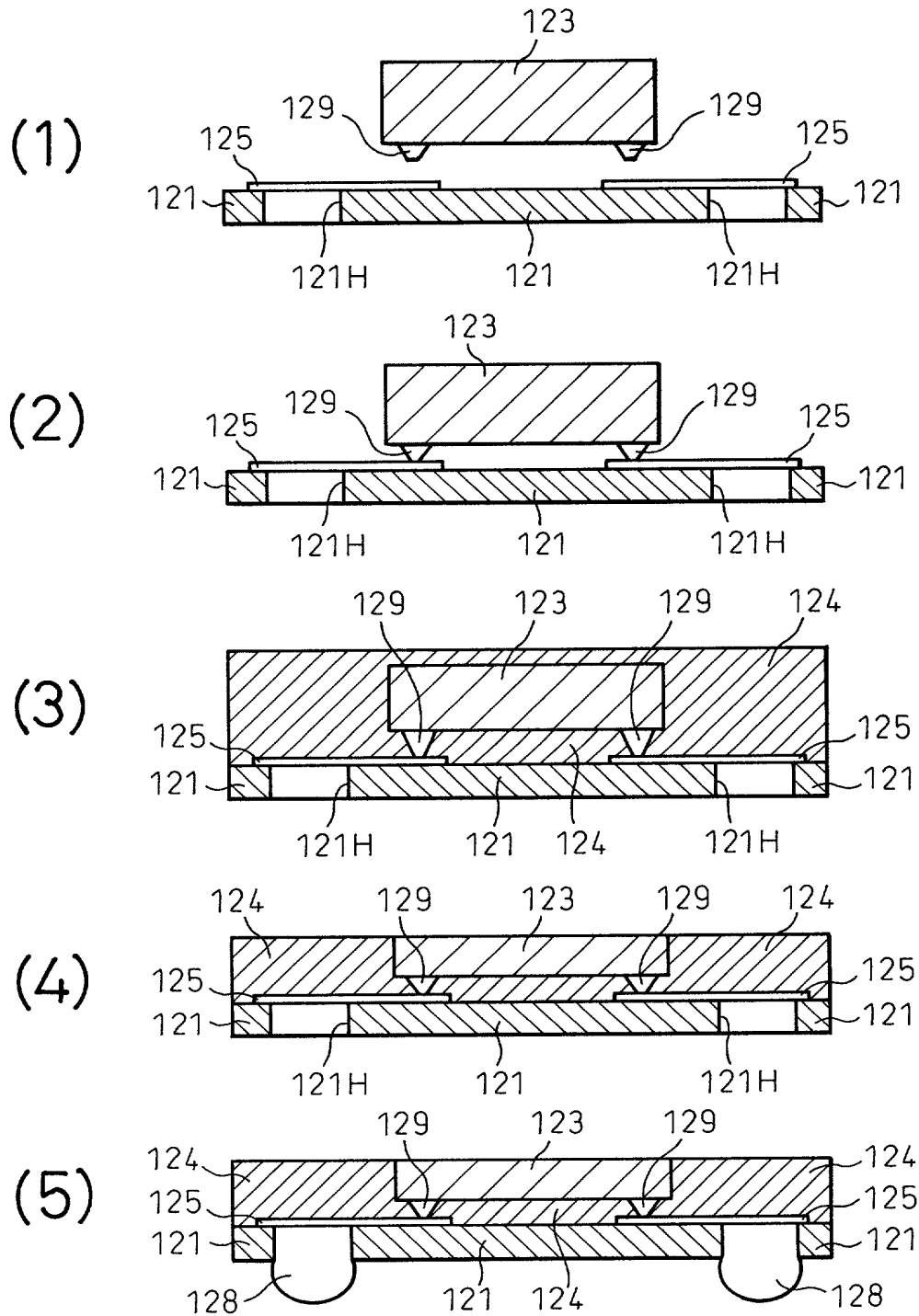
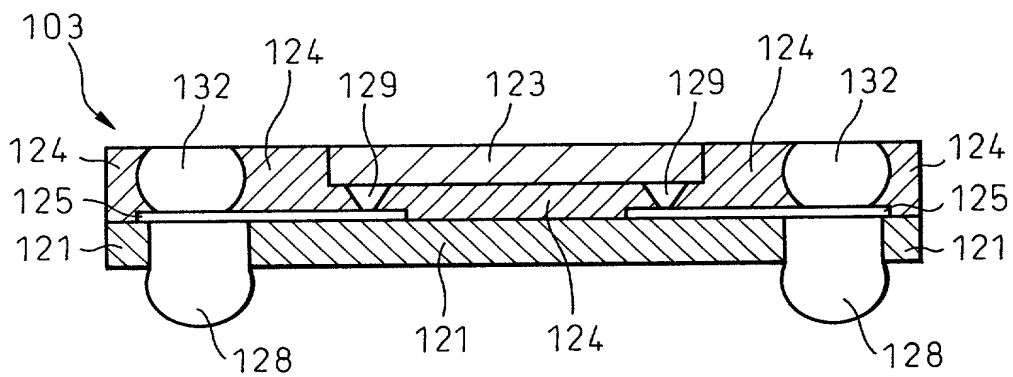


Fig.28

(1)



(2)

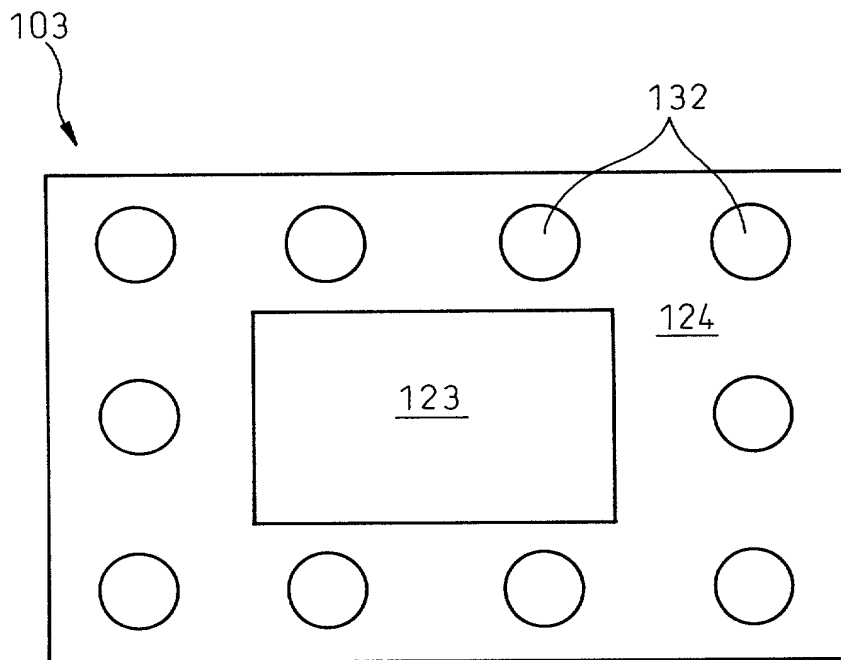
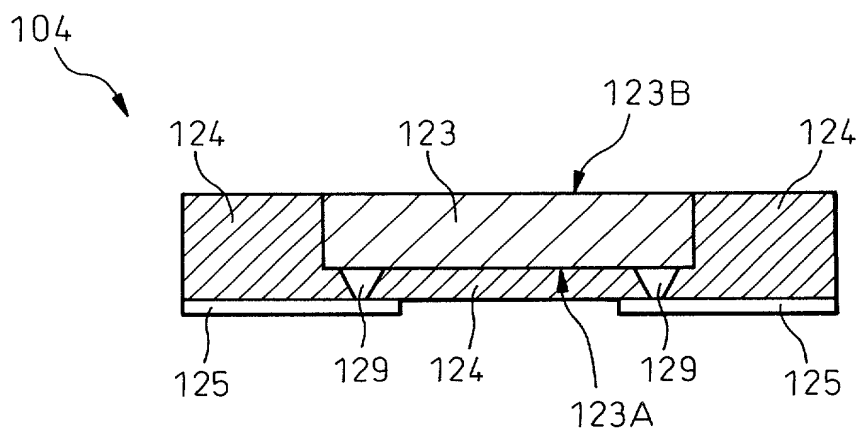


Fig.29

(1)



(2)

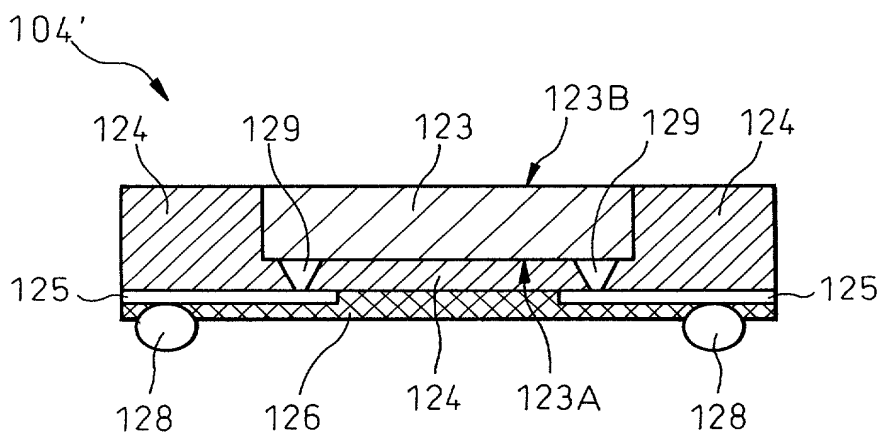


Fig.30

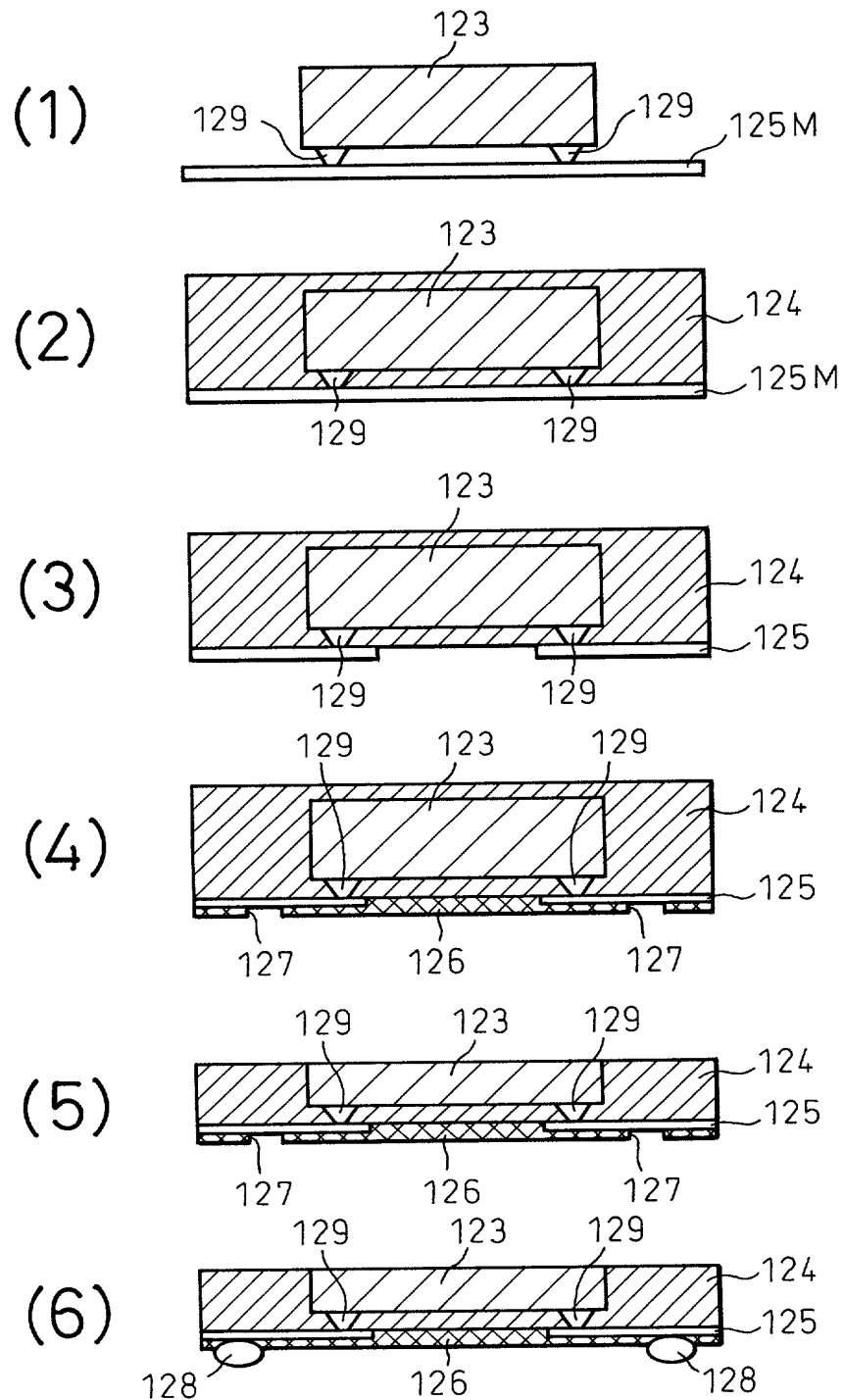


Fig.31

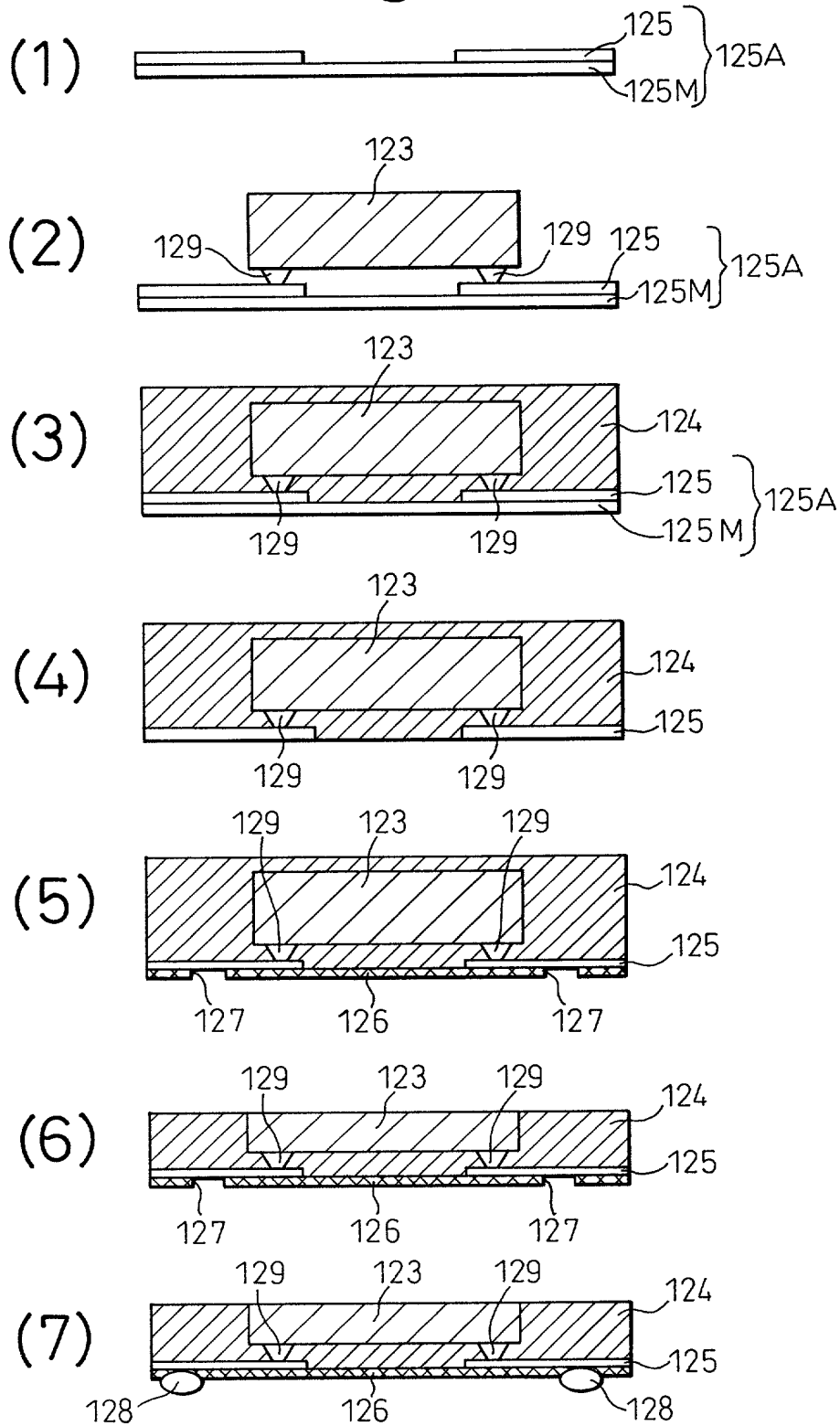
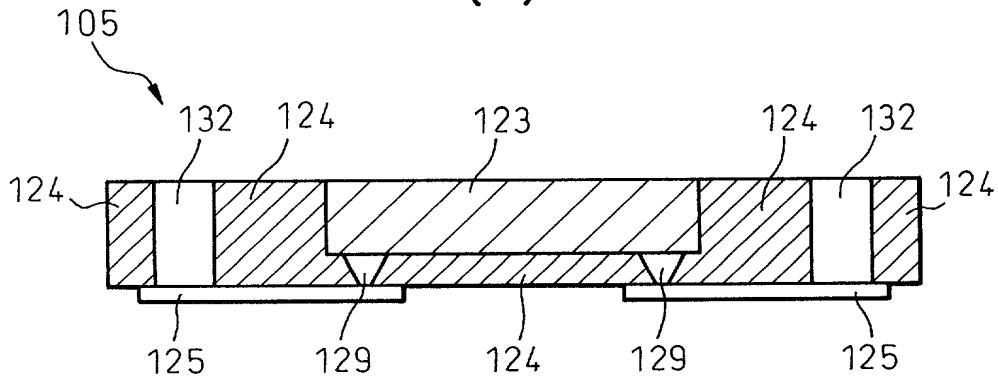
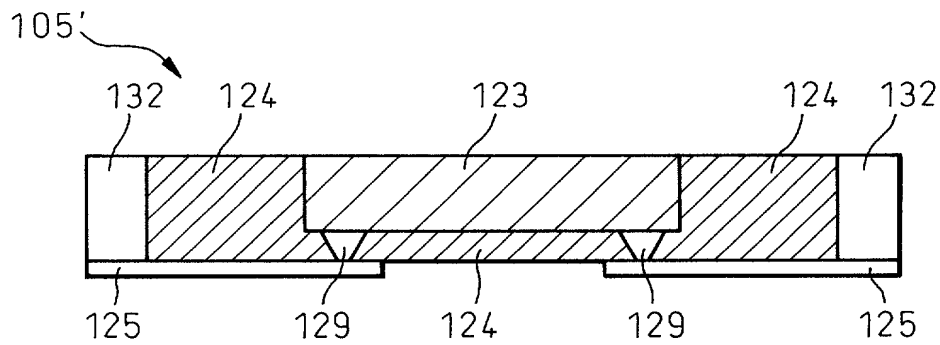


Fig.32

(1)



(2)



(3)

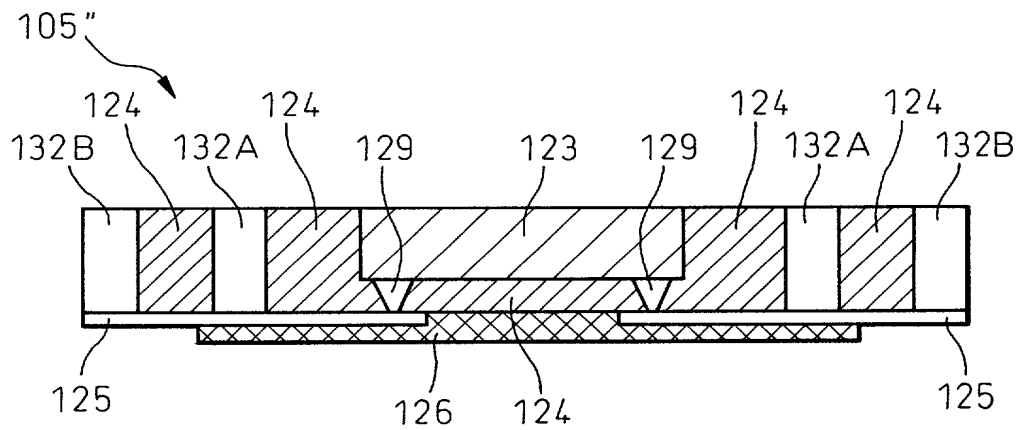
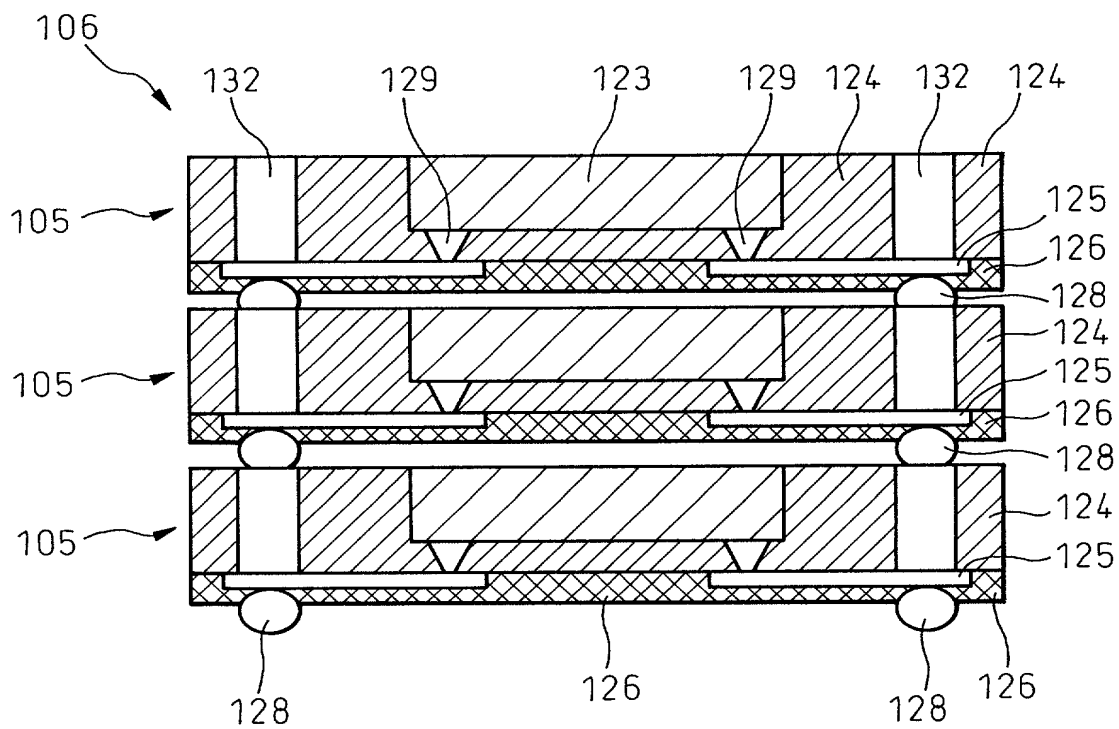


Fig.33





(1)

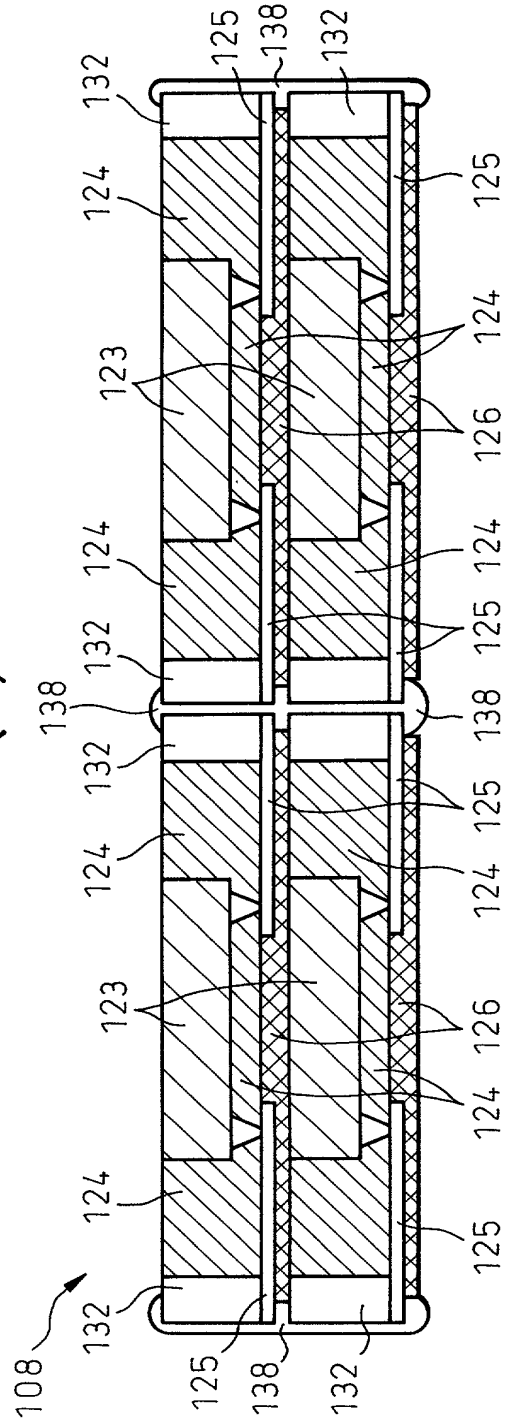
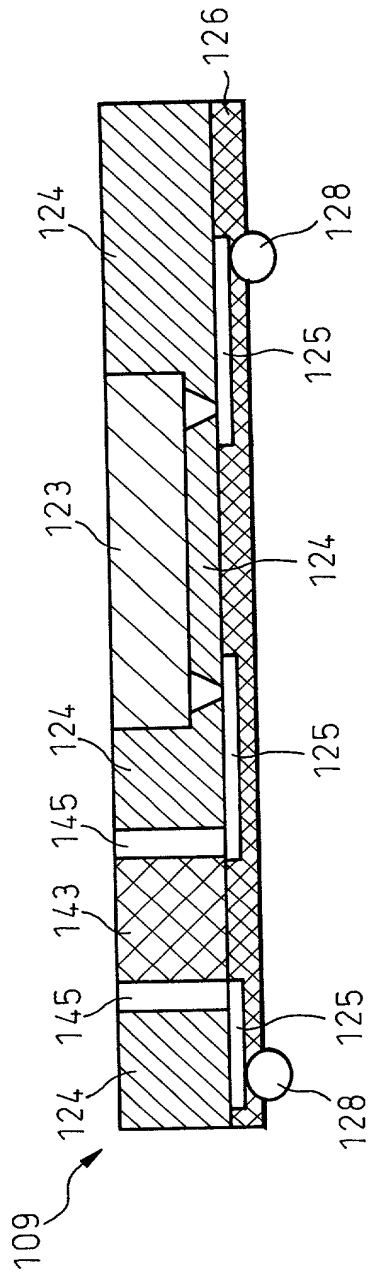


Fig. 35

(1)



(2)

